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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/765,510
Priority Filing Date January 19, 2001
Inventor Kunal R. Parekh et al.
Assignee Micron Technology, Inc.
Priority Group Art Unit 2813
Priority Examiner Y. Huynh
Attorney's Docket No. MI22-1816
Title: Capacitors, DRAM Arrays, Monolithic Integrated Circuits, And Methods of Forming
Capacitors, DRAM Arrays, And Monolithic Integrated Circuits

11000 U.S. PTO
09/955632
09/18/01

INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

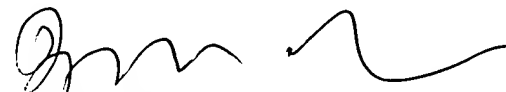
The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation application of co-pending application Serial No. 09/765,510, filed January 19, 2001, upon which the above-identified application relies for a priority date under 35 U.S.C. §120. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. §1.98(d) and MPEP §609(2). As a courtesy, Applicant submits copies of the cited references for review.

Citation of these references is respectfully requested.

Respectfully submitted,

Date:

Sept. 18, 2001



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